

**REMARKS**

New claim 22 is added herein. Claims 1-22 are pending and under consideration.

The rejections based on Akiyama are now discussed. Using independent claim 1 as an example, this claim recites a system bus to directly connect the engine control unit with the processor. As discussed at pages 1-3 of the present Specification, interfaces (i.e., 14 and 32 of the related art shown in present Fig. 1) are undesirable, and therefore, have been replaced by the present system bus.

This distinction was previously pointed out to the Examiner. In the Advisory Action mailed January 25, 2008, the Examiner indicated that Fig. 1 of Akiyama shows that the Video I/F 24 connects the engine unit 25 and the CPU 16. The Examiner appears to conclude that any connecting element qualifies as a "bus." However, this is not the case. For example, the interfaces 14 and 32 connect the CPUs 31 and 15, however, these elements are not buses.

Also, claim 4 recites a memory to store state information about the engine mechanism. However, memory 204 of Kim stores print data or bitmap data transferred from the computer. Kim, col. 4, ln. 57-61 and col. 5, ln. 1-6.

New claim 22 recites the transmitted image data is transmitted via the bus without processing by the bus. In contrast, because the video I/F 24 of Akiyama is an interface, as opposed to a bus, the I/F 24 would perform processing on data transmitted thereby.

Accordingly, withdrawal of the rejections is requested.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

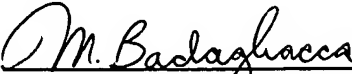
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If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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